

		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1427		SERIAL NO. 09/559,903	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Zhiping Yin et al			
				FILING DATE April 26, 2000		GROUP 2815	
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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							Yes No
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/PB/	A R		Wolf, S., "Silicon Processing for the VLSI Era," Vol. 1, pps. 407-413.		1986 Lattice Press		
/PB/	AS		Wolf, S. "Silicon Processing for the VLSI Era," Vol. 2, pgs. 48-49 and 435.		1986 Lattice Press		
EXAMINER /Paul Budd/ DATE CONSIDERED 05/15/2009							
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							